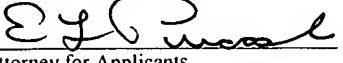


IZW

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Postal Services as First-Class Mail in an
envelope addressed to:

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Attorney for Applicants



Date: 26 June 2008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/766,386
Applicants: Swanson, Travis et al.
Filed: 28 January 2004
Title: Generation of Memory Test Patterns for DLL Calibration
Art Unit: 2117
Examiner: CHUNG, Phung M.
Docket No.: PAT001098-000; 013721-0037-999

TRANSMITTAL LETTER FOR
SUPPLEMENTAL APPLICATION DATA SHEET

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith is a Supplemental Application Data Sheet in accordance with MPEP § 605.04(b) containing a change to the inventors' names wherein a typographical error omitted the inventors' middle initials. Appropriate correction is respectfully requested.

The Commissioner is hereby authorized to charge any fees due in connection with this filing to our Deposit Account No. 50-3013. A copy of this Transmittal Letter is enclosed.

Respectfully submitted,

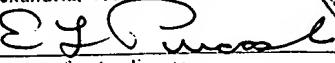


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Dated: 26 June 2008

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Attorneys for Applicants

Dated: 26 June 2008

SUPPLEMENTAL APPLICATION DATA SHEET



BIBLIOGRAPHIC DATA

1. Applicant information

First Applicant

Applicant Authority Type:: Inventor
Primary Citizenship Country:: United States
Status:: Full Capacity
Given Name:: Travis
Middle Name:: E.
Family Name:: Swanson
Name Suffix::
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Country of Residence:: United States
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City of Mailing Address:: Ramsey
State/Province of Mailing Address:: MN
Country of Mailing Address:: United States
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Second Applicant

Applicant Authority Type:: Inventor
Primary Citizenship Country:: United States
Status:: Full Capacity
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Family Name:: Greeff
Name Suffix::
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State/Province of Residence:: ID
Country of Residence:: United States
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State/Province of Mailing Address:: ID
Country of Mailing Address:: United States
Postal or Zip Code of Mailing Address:: 83709

2. Correspondence information

Correspondence Customer Number:: 20583

3. Application Information

Application Number:: 10/766,386
Filing Date:: 01/28/2004
Application Type:: Utility
Suggested Classification:: None
Suggested Technology Center:: None
CD-ROM or CD-R?:: None
Number of CDs:: 0
Number of CD copies:: 0
Sequence Submission?:: None
Computer Readable Form?:: No
Number of CRF copies:: 0
Title:: Generation of Memory Test Patterns for DLL
Calibration
Attorney Docket Number:: PAT1098-000; 013721-0037-999
Request for Early Publication?:: No
Request for Non-Publication?:: No
Suggested Drawing Figure:: 1
Total Drawing Sheets:: 3

Small Entity?:: No
Latin Name::
Variety Denomination Name:
Petition Included?:: No
Petition Type::
Licensed US Govt Agency::
Contract or Grant Numbers::
Secrecy Order in Parent Application:: No

4. Representative information

Representative Customer Number:: 57694

5. Domestic Priority information

Application::
Continuity Type::
Parent Application::
Parent Filing Date::
Parent Application Status::

6. Foreign Priority information

Country::
Application Number::
Filing Date::
Priority Claimed::

7. Assignee information

Assignee Name:: Micron Technology, Inc.
Street Mailing Address:: 8000 S. Federal Way
City of Mailing Address:: Boise
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Country of Mailing Address:: United States

Postal or Zip Code of Mailing Address:: 83707-6006